

EE382N-20 Fall 2016

Homework 1

Work in groups of 3 - 5; some of this is likely to be on the exam so don't just tag along without contributing and understanding!

Review the slides from class:

https://lph.ece.utexas.edu/class/Fa16EE382N/Lect4Notes?action=download&upname=Lecture04_HWPar.pdf

Now fill in the tables below to your best ability and after extensive discussion within your group and actually also Piazza. I expect a very very very short explanation for only those boxes you had to discuss at some length.

Style	Synchronization	Communication
Superscalar		
VLIW		
Dataflow		
SIMD		
MIMD		

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Summary of sharing in ILP HW

Style	Seq	Inst	OOO	Regs	Mem	ALUs	Net
Superscalar							
SMT/TLS							
VLIW							
Dataflow							

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Summary of sharing in DLP and TLP

Style	Seq	Inst	OOO	Regs	Mem	ALUs	Net
Vector							
SIMD							
MIMD							