



# Toward Exascale Resilience Part 8: Containment Domains / cross-layer schemes

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#### **Credit to:**

#### UT Austin students:

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#### Collaborators (growing list)

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- LBNL: Brian Austin, Dan Bonachea, Paul Hargrove , Sherry Li, Eric Roman

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- DOE ECRP, XStack, FF, PSAAP II
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#### The **constraints**:

- Power/energy
- Time
- Money
- Correctness





# **Resilience** is a big challenge for **DOE computations**







Year (Expected performance in PetaFlops)

Something **bad** every ~**minute at DOE** scale



## The baseline: checkpoint-restart

Not good enough on its own







Failure rate too high for checkpoint/restart Correctness also at risk







#### Energy also problematic



#### The **cost** of resilience

- Preparation
- Detection
- Mitigation (repair + recover)
- Implementation





Software? Hardware? Algorithm?



Software? Hardware? Algorithm?

#### Containment Domains: adaptive **holistic** approach – Per-experiment balance of energy, time, money, correctness



# Can **hardware alone** solve the problem? Yes, but **costly**

- Significant and likely fixed overheads
- May not be needed in many commercial settings



### Fixed overhead examples (estimated) Both energy and/or throughput

- Up to ~25% chipkill **correct vs.** chipkill **detect**
- 20 40% for pipeline SDC reduction
- >2X for arbitrary correction
- Even greater overhead if protecting approximate units



# Something bad every ~minute at DOE

# Something bad every year commercially

- Smaller units of execution
- Different requirements



### Locality and hierarchy are key

- Hierarchical constructs
- Distributed operation

Range of correctness requirements



# What about **algorithmic resilience**?

- Algorithmic detection
- Iterative converging algorithms
- Redundant information
- Probabilistic methods

NZ



#### Examples on board

- Algorithmic check of matrix multiplication
- Algorithmic check of a solver
- Convergent calculation
  - Simple and basic Newton-Raphson
- Monte Carlo





#### But,

### Different apps → different techniques Different scales → different techniques





#### Need to adapt/co-tune



# Containment Domains elevate resilience to **first-class abstraction**

- Program-structure abstractions
- Composable resilient program components
- Regimented development flow
- Supporting tools and mechanisms



#### **Containment Domains**

- Abstract resilience constructs that span system layers
- Hierarchical and Distributed operation for locality
- Scalable to large systems with high energy efficiency
- Heterogeneous to match disparate error/failure effects
- Proportional and effectively balanced
- Tunable resilience specialized to application/system
- Analyzable and auto-tuned





# **CDs Embed Resilience within Application**

#### Express resilience as a tree of CDs

- Match CD, task, and machine hierarchies
- Escalation for differentiated error handling

#### Semantics

- Erroneous data never communicated
- Each CD provides recovery mechanism
- Components of a CD
  - Preserve data on domain start
  - **Compute** (domain body)
  - **Detect** faults before domain commits
  - *Recover* from detected errors



























#### Concise abstraction for complex behavior







### Programming and execution model support





#### CDs manage preservation, restoration, and re-execution

- Allocate and frees storage
- Transfer data
- Manage default error detection
- Call appropriate CD (hierarchy level) on error/fault
- Holistic error reporting

#### Specific policies can be written by the user

- Specialize and tune every aspect of resilience
- Straightforward abstractions

#### CD abstraction amenable to analysis and auto-tuning

– Analytical model fed with application properties





#### **CD** Runtime System Architecture **External Tool** Internal Tool **Future Plan CD**-annotated **Compiler Support** Debugger **Applications/Libraries** User Interaction for customized error CD-App detection /handling / tolerance / injection Mapper **CD** Runtime System ] [ Scaling Tool **Persistence Layer** (LWM2) Ĵ Runtime Error **Unified Runtime** Communication Auto-tuner State Preservation Logging Logging Error Detector Interface Handling CD Auto Profiling & Low-Level BLCR Communication Tuner Legion + **CD-Storage** Visualizatio Machine Check **Runtime Library** Libc n Interface (Legion + GasNet) HW/SWI/F **Mapping Interface** Sight SSD HDD Error Reporting Hardware

- Annotations, persistence, reporting, recovery, tools



# CD usage flow

- Annotate
- Profile and extrapolate CD tree
- Supply machine characteristics
- Analyze and auto-tune
  - Flexible preservation, detection, and recovery
- Refine tradeoffs and repeat
- Execute and monitor
  - CD management and coordination
  - Distributed and hierarchical preservation
  - Distributed and hierarchical recovery



# CD annotations express **intent**

- **CD hierarchy** for scoping and consistency
- Preservation directives and hints exploit locality
- Correctness abstractions
  - Detectors and tolerances
- **Recovery** customization
- Debug/test interface

Work in progress: http://lph.ece.utexas.edu/users/CDAPI



# 

- Hierarchical
  - Per CD (level)
  - Match storage hierarchy
  - Maximize locality and minimize overhead
- Proportional
  - Preserve only when worth it (skip preserve calls)
  - Exploit inherent redundancy
  - Utilize regeneration





Partial preservation via sibling, parent, or regeneration where appropriate





#### Local copy or regen



#### Parent (unchanged)









#### **Correctness abstractions**

- Detectors
- Requirements
- Recovery




## What can go **wrong**?

- Application crash
- Process crash
- Process unresponsive
- Failed communication
- Hardware
  - Cache error
  - Memory error
  - TLB error
  - Node offline





## What can go **wrong**?

- Lost resource
- Wrong value
  - Specific address?
  - Specific access?
  - Specific computation?
- Degraded resource

## Who detects? How reported?





## Today: machine check architecture

- (Maskable) interrupts
- Complex encoding of errors / failures
  - Spread across many processor-specific state registers
  - Very difficult to parse and use
- Currently level of containment reported
  - Enables fine-grained software recovery
  - Know before state is corrupted
  - Know when only process state is corrupted
- Event counters and triggers for errors
  - Root cause analysis





## Today: machine check architecture

- Not suitable for programmers
  - Barely suitable for system implementers
  - Doable, but tricky and requires a lot of reading
- Varies by vendor
- Continuously updated



## System-provided detectors

- curCD->Detect();
  - Control response granularity

## User-specified detectors

- curCD->

CDAssert(test, error\_to\_report);

Consistent and unified reporting & analysis





## Catch the error as soon as possible

- Less to recover
- Ideally smaller and faster preservation
- Micro-rollbacks
- Idempotent regions
- Hardware-level rollbacks





## Idempotent regions and hardware-rollback

- What if hardware can automatically rollback and rexecute?
  - Fine-grained recovery will have little impact on performance
  - Users may not need to do anything





## Instruction retry

- Out-of-order processors
- In-order and GPUs?





## Sophisticated out-of-order offer ample opportunity for hardware retry

- Speculative execution can be used to recovery from soft errors
- ROB and LSQ buffer temporary results
- Transactional memory does to

Nz



## Harder in a GPU

- Need to ensure effect-free rollback
  - No hardware buffering
- Idempotent regions and CDs
- Tradeoffs with hardware buffering and detection
  latency





## Express correctness intent

- curCD->
  - RegisterDetection(errors\_reported);
  - Notifies auto-tuner of detection capability
  - Enables error elision
- curCD->RequireErrorProbability(
  error\_type, num\_errors,
  probability,detect\_or\_fail\_over);
  - Auto- add redundancy to meet requested level of reliability
- curCD->GetErrorProbability(

error\_type, num\_errors);

Customize action





## Analogues to approximate computing research

- Compiler techniques for approximate computing
- Propagate loss of accuracy
- Propagate loss of reliability



## Debug, test, and tools

- Error and failure injection
- Planned integration with low-level injection
- CD profiler, viz, models, and initial tuner in place





Search

## Quick(ish) way to search the error space

- Multi-mode simulation
- Skip over detectable errors



- Tool to be released
  - Uses only public tools





CD Graph corresponding to profile outputcd\_profile\_2\_1\_2.conf [# of level = 3] CD\_0\_0 Loop count: 1 Exec cycle: 37689 Preserved data: 8800 Overlapped data: 4800 CD 1 0 CD\_1\_1 Loop count: 1 CD 1 2 CD 1 3 Loop count: 1 Loop count: 1 Loop count: 1 Exec cycle: 37689 Exec cycle: 37689 Exec cycle: 37689 Exec cycle: 37689 Preserved data: 8800 Preserved data: 8800 Preserved data: 8800 Preserved data: 8800 Overlapped data: 4800 Overlapped data: 4800 Overlapped data: 4800 Overlapped data: 4800 CD\_2\_1\_2 CD\_2\_3\_0 CD\_2\_3\_3 CD\_2\_0\_0 CD\_2\_0\_1 CD\_2\_0\_2 CD\_2\_0\_3 CD\_2\_1\_0 CD 2 1 1 CD\_2\_1\_3 CD\_2\_2\_0 CD\_2\_2\_1 CD\_2\_2\_2 CD\_2\_2\_3 CD\_2\_3\_1 CD\_2\_3\_2 Loop count: 1 Loop coum Loop count: 1 Exec cycle: 37689 Exec cy Exec cycle: 37689 data: 8800 Preserved data: 8800 i data: 4800 Overlapped data: 4800 CD 1 0 Loop count: 1 Exec cycle: 37689 Preserved data: 8800 Overlapped data: 4800 CD\_2\_0\_1 CD\_2\_0\_0 CD\_2\_0\_2 CD\_2\_0\_3 Loop count: 1 Loop count: 1 Loop count: 1 Loop count: 1 Execcycle: 37689 Exec cycle: 37689 Exec cycle: 37689 Exec cycle: 37689 Preserved data: 8800 Preserved data: 8800 Preserved data: 8800 Preserved data: 8800 Overlapped data: 4800 Overlapped data: 4800 Overlapped data: 4800 Overlapped data: 4800



## Machine and error models



- L0 Local DRAM: 483\*9GB/sec

- L1 Remote DRAM: 483GB/sec

- L3 Disk: 2.1GB/sec

Component	"Performance"	Error	Error Scaling
Core	10GFLOP/core	Soft error	∝ #cores
Memory	1GB/core	ECC fail	∝ #DRAM chips
Socket	200GB/s /socket	Hard/OS crash	∝ #sockets
System	Hierarchical network	Power module or network	∝ #modules and #cabinets





#### Input 1: machine configuration

- Physical and storage hierarchies (capacity and BW)
- Error/failure rates at each level of hierarchy
- Simple power model
- Input 2: application description
- CD tree, including loops of CDs
- Preservation volumes and possible method
- Overlap of preservation and detection with parent
- Execution time estimate
- Analytic model for CD behavior
- Overheads from preservation, detection, and recovery
- Output efficiency
- Performance, energy, memory



## Error Failure Recovery



Containment Domains DEGAS/ExMatEx March 2014 Leverage hierarchy and CD semantics

− Solve in  $\rightarrow$  out

#### Application abstracted to CDs

- CD tree
- Volumes of preservation, computation, and communication
- Preservation and recovery options per CD
- Machine model
  - Storage hierarchy
  - Communication hierarchy
  - Bandwidths and capacities
  - Error processes and rates







## **Power model**

CDs that are not re-executing may remain idle Actively executing a CD has a relative power of 1

A node that is idling consumes a relative power of  $\alpha$ 

- In our experiments  $\alpha = 0.25$ 



![](_page_56_Picture_0.jpeg)

![](_page_56_Picture_1.jpeg)

## SPMD-oriented analytical model and tuner

- Extrapolated profile
- Machine characteristics
- Tuning space and models

![](_page_56_Figure_6.jpeg)

![](_page_57_Picture_1.jpeg)

![](_page_57_Picture_2.jpeg)

## Auto-tuned cross-layer resilience!

- Iterate with error injection
- Intelligent search exploration

![](_page_58_Picture_0.jpeg)

![](_page_58_Picture_1.jpeg)

## Execution model progress

- Building systems is hard and tricky
- Limited release of single-node runtime
- MPI runtime very close
  - Lots of distributed programming issues
  - Lots of current sad state of FT issues
- Open source soon on Bitbucket
  - Initially only for soft errors

![](_page_59_Picture_1.jpeg)

## Already useful and collaborations in progress

- Reaching down to hardware in FF2
- Global address space with DEGAS
- Task-based execution in Legion and SWARM
- DSL-facing in Stanford's PSAAP II
- Algorithmic approach within TOORSES

![](_page_60_Picture_0.jpeg)

![](_page_60_Picture_2.jpeg)

## TOORSES fault-tolerant hierarchical solver

- Brian Austin, Eric Roman, and Xiaoye (Sherry) Li
  LBNL
- Hierarchical semi-separable representation

![](_page_60_Figure_6.jpeg)

![](_page_61_Picture_1.jpeg)

Add CDs at different granularities – Hierarchical and partial preservation Add algorithmic and cheap detection Compare to:

Algorithmic recovery with redundant computation

![](_page_61_Figure_4.jpeg)

![](_page_62_Picture_1.jpeg)

## LULESH CD mapping example

![](_page_62_Figure_3.jpeg)

![](_page_63_Picture_0.jpeg)

![](_page_63_Figure_2.jpeg)

40% 20%

0%

![](_page_63_Figure_3.jpeg)

![](_page_63_Figure_4.jpeg)

■ CDs, NT

■ h-CPR, 80%

■ g-CPR, 80%

![](_page_63_Figure_5.jpeg)

![](_page_64_Picture_1.jpeg)

![](_page_64_Figure_2.jpeg)

![](_page_64_Figure_3.jpeg)

Peak System Performance

![](_page_65_Picture_1.jpeg)

## 10X failure rate emphasizes CD benefits

![](_page_65_Figure_3.jpeg)

![](_page_66_Picture_1.jpeg)

![](_page_66_Picture_2.jpeg)

## What if my application has many barriers? – Can't really form a tree?

![](_page_67_Picture_0.jpeg)

![](_page_67_Picture_2.jpeg)

# SPMV: local recovery and partial preservation

![](_page_67_Figure_4.jpeg)

Partial preservation via sibling or parent where appropriate

![](_page_68_Picture_1.jpeg)

## Inter-CD communication?

#### Strict CDs do not communicate

- Only communicate when in same CD context
- Overheads for strict containment can be high

## Relaxed CDs enable inter-CD communication

- Maintain CD semantics w/ uncoordinated recovery
- Some data "preserved" via logging
- All communicated data still verified to be correct

![](_page_68_Figure_10.jpeg)

![](_page_69_Picture_1.jpeg)

## SPMV: local recovery and partial preservation

![](_page_69_Figure_3.jpeg)

Partial preservation via sibling or parent where appropriate

![](_page_70_Picture_2.jpeg)

## Fun with logging protocols

![](_page_70_Figure_4.jpeg)

![](_page_71_Picture_1.jpeg)

![](_page_71_Picture_2.jpeg)

### What about tasks?

- CDs are great natural fit
  - CDs + Legion
    - Stanford project led by Alex Aiken
  - CDs + Swarm
    - Spinoff from UDel led by Guang Gao
  - Perhaps also with \*SS / Nachos
    - Barcelona Supercomputing Centers




## Legion resilience

- Propagate failures up the dependence chain
- Utilize region copies to minimize reexecutions







### Legion + CDs resilience

- Model-guided management of copies
- Optimized reexecution propagation stop points
- Detection and specification semantics
- Integration with other resilience mechanisms







### Use Legion copies for CD preservation

### Optimize for efficiency

- When to add copies
- Where to put copies to survive failures
- When to free copies

# Account for different failure modes and rates









(h) Markov chain model of (f) and (g)



Assumption/fear: reliability bounds performance

- Errors may corrupt results and failures kill applications
  What is the error rate?
- Like today: keep ignoring the problem
- Much higher: need detection and recovery
- CDs abstract, scalable, and tunable
- What is the failure rate?
- Like today: hierarchical checkpoint restart
- Higher: specialize preservation and recovery
- CDs are portable and tunable
- Is it really a problem?
  - CDs are general and analyzable
  - CDs are **composable**?



# Conclusion

### **Containment domains**

- Abstract constructs for resilience concerns & techniques
- **Proportional** and application/machine tuned resilience
- Hierarchical & distributed preservation, and recovery
- Analyzable and amendable to automatic optimization
- **Scalable** with high relative energy efficiency
- Heterogeneous to match emerging architecture



http://lph.ece.utexas.edu/public/CDs



#### Thank You!

 Please find the slides at https://lph.ece.utexas.edu/merez/MattanErez/Exacale ResilienceShort0715